

REMARKS

Claims 1-11 are pending in the application. Claims 1, 3, 4, 7, and 8 stand rejected 35 U.S.C. 103(a). Specifically, Claims 1, 3-4, and 7 were rejected under 35 U.S.C. 103(a) as being unpatentable over US 6,982,432 by Umemoto et al., in view of US Publication 2001/0046604 by Geaghan, US 6,819,316 by Schulz et al, and US 6,534,200 by Heuer et al.

Claim 8 was rejected under 35 U.S.C. 103(a) as being unpatentable over US 6,982,432, in view of of US Publication 2001/0046604, US 6,819,316, US 6,534,200, and further in view of US 6,229,506 by Dawson et al.

Applicants have amended independent claim 1 to explicitly claim the low temperature sputtering process for indium tin oxide (ITO) in forming a touch screen upon one side of a transparent substrate and a bottom emitter non-transparent OLED on the other side of the transparent substrate. Claims 2-4, 7, and 11 have been canceled. Reconsideration and allowance of the claims as amended is requested for the following reasons.

The present invention is now directed to method of manufacturing a display device having an OLED display and a touch screen, the OLED display including components that are sensitive to high temperatures, and the touch screen including a resistive film, comprising the steps of:

- a) providing a transparent substrate having two sides;
- b) forming a bottom-emitting, non-transparent flat-panel organic light emitting diode display on a first side of the transparent substrate that emits light through the transparent substrate;
- c) forming a transparent resistive film using a low temperature ITO-sputtering without any annealing step on the second side of the substrate, opposite the first side of the substrate; and
- d) forming a resistive touch screen on the transparent resistive film through which light is emitted from the OLED display, and wherein the transparent resistive film is formed on the second side of the substrate in step c) after forming the organic light emitting diode display on the first side of the substrate in step b), and wherein the low temperature ITO-sputtering without any annealing does not subject the substrate to temperatures higher than 150°C.

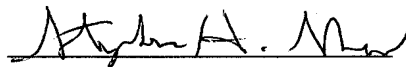
Applicants wish to point out the inventive process they undertook in recognizing that a touch screen can be formed on a transparent substrate using alternative, non-conventional processes that are compatible with organic materials only found in OLED devices and not LCDs. Notably, the conventional method of sputtering ITO in an optimal fashion would require high substrate and annealing temperatures that are not practical in Applicants' novel concept.

Applicants have skillfully recognized that low temperature sputtering can be accomplished without detrimental substrate heating and/or an annealing step that is also normally required at the high temperatures for conventional ITO sputtering. Therefore, those skilled in the art reading written disclosures of conventional ITO sputtering would be taught that the ITO should be sputtered at high temperatures and include an annealing step for optimal effectiveness. In other words, the written disclosures teach away from low temperatures of 150 C without annealing, if they want their LCD device to be fabricated without defect. Inherent in Applicants' novel method is that the length of time for sputtering the ITO is greatly reduced, because of the low temperature and the absence of the annealing step.

Applicants have reviewed the cited art made of record and believe that singly or in any suitable combination, they do not render Applicants' claimed invention unpatentable. It is believed that the claims in the application are allowable over the cited art and such allowance is respectfully requested.

Should the Examiner consider that additional amendments are necessary to place the application in condition for allowance, the favor is requested of a telephone call to the undersigned counsel for the purpose of discussing such amendments.

Respectfully submitted,



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If the Examiner is unable to reach the Applicant(s) Attorney at the telephone number provided, the Examiner is requested to communicate with Eastman Kodak Company's Patent Operations at (585) 477-4656.